

Remarks

In the Office action dated March 27, 2006, claims 1-13 were rejected. Claims 1 and 11 were rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Pat. No. 5,991,223 ("Kozaru et al."). Claims 5 and 8 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Kozaru et al. in view of U.S. Pat. No. 6,111,808 ("Khang et al."). Dependent claims 2-4, 7, and 9-10 were objected to for being dependent on a rejected base claim but would be allowable if rewritten in independent form including the limitations of the base and any intervening claims. Claims 14 and 15 were allowed. The rejections and objections will be discussed below.

Interview

Applicants thank the Examiner for conducting telephone interviews on May 19 and 22 to clarify the points of rejection with regard to the prior art.

Amendments

Applicants have amended independent claims 1, 8, and 11. Independent claim 1 has been amended to clarify the claim. Independent claim 8 has been amended by incorporating dependent claim 9 and correcting typographical errors. Independent claim 11 has been amended by incorporating dependent claim 12. According to the Office action, amended claims 8 and 11 are now in allowable form.

§ 102(b)

Claim 1 rejected under § 102(b) as being anticipated by Kozaru et al. in order to anticipate a claim, a reference must teach all the elements of a claim. See Verdegaal Bros., Inc. v. Union Oil Co., 814 F.2d 628, 631 (Fed. Cir. 1987). In addition, the reference must show the claimed invention "in as

complete detail as in contained in the patent claim" in order to anticipate the claimed invention. Richardson v. Suzuki Motor Co., Ltd., 868 F.2d 1226, 1236 (Fed. Cir. 1989).

Applicant asserts that Kozaru et al. fails to teach each of the elements of Applicants' claims and does not show the claimed invention in as complete detail as contained in the patent claim; therefore, the claim is not anticipated.

Kozaru et al. fails to teach amended claim 1's element of "shifting out each word of said first group of words of said plurality of words synchronously at the end of said latency period, one word of the first group of words shifted out at each clock cycle following the latency period until each word of the first group of words has been shifted out." Amended claim 1. Kozaru et al. teaches that in burst mode, each memory block may read and output one word, and each memory block's word may be output in parallel with other memory blocks. See, e.g., Kozaru et al., claim 33. However, Kozaru et al. does not teach a method of reading a first group of words into a plurality of data registers during a latency period and then "shifting out each word of said first group of words of said plurality of words synchronously at the end of said latency period, one word of the first group of words shifted out at each clock cycle following the latency period until each word of the first group of words has been shifted out." Amended claim 1. Therefore, Kozaru et al. does not teach each element of Applicants' claim 1. A withdrawal of this rejection is requested.

Conclusion

Applicants have amended claims 1, 8, and 10, and 11. Claims 5-7 were cancelled. A Notice of Allowance is requested.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Respectfully submitted,



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